



SHEET 1 OF 1

Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO. 244186US2		SERIAL NO. 10/687,629	
LIST OF REFERENCES CITED BY APPLICANT				APPLICANT Satoshi INOUE			
				FILING DATE October 20, 2003		GROUP 2812	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	AA						
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	AK						
	AL						
	AM						
	AN						
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES NO		
	AO	2001-92662	04/06/2001	JAPAN			X
	AP	8-137661	05/31/1996	JAPAN			X
	AQ	8-263290	10/11/1996	JAPAN			X
	AR						
	AS						
	AT						
	AU						
	AV						
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)							
	AW						
	AX						
	AY						
	AZ						
Examiner					<input type="checkbox"/> Additional References sheet(s) attached		
Date Considered							
*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							



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FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES NO		
	AO	8-69377	03/12/1996	Japan (with English Abstract)			X
	AP	2001-43084	02/16/2001	Japan (with English Abstract)			X
	AQ	9-212358	08/15/1997	Japan (with English Abstract)			X
	AR	5-242050	09/21/1993	Japan (with English Abstract)			X
	AS						
	AT						
	AU						
	AV						
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10/20/03

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LIST OF REFERENCES CITED BY APPLICANT				APPLICANT <b>Satoshi INOUE</b>				
				FILING DATE Herewith			GROUP	
U.S. PATENT DOCUMENTS								
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE	
	AA							
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	AQ							
	AR							
	AS							
	AT							
	AU							
	AV							
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)								
	AW	Francesco LERTORA, et al., "A CUSTOMIZED PROCESSOR FOR FACE RECOGNITION", Embedded Processor Forum, May 1, 2002, pgs. 1 - 8.						
	AX	Michele BORGATTI, et al., "A RECONFIGURABLE SYSTEM FEATURING DYNAMICALLY EXTENSIBLE EMBEDDED MICROPROCESSOR, FPGA AND CUSTOMISABLE I/O", IEEE 2002 Custom Integrated Circuits Conference, pgs. 13 - 16.						
	AY							
	AZ						<input type="checkbox"/> Additional References sheet(s) attached	
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